L Number	Hits	Search Text	DB	Time stamp
1	200	(725/39).CCLS.	USPAT	2004/09/17 14:36
2	78	(725/95).CCLS.	USPAT	2004/09/17 14:37
3	785	(709/232 709/233).ccls.	USPAT	2004/09/17 14:37
4	1	(bit adj rate$5) with (reserv$6 allocat$6) with (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:39
5	3	((725/39).CCLS.) and ((709/232 709/233).ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:38
6	2	((725/95).CCLS.) and ((709/232 709/233).ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:38
9	7	(bit adj rate$5) with (frequenc$6 bandwidth) with (reserv$6 allocat$6) and (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:40
12	20	(bit adj rate$5) same (frequenc$6 bandwidth) same (reserv$6 allocat$6) and (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:40
14	0	((bit adj rate$5) same (frequenc$6 bandwidth) same (reserv$6 allocat$6) and (epg (electronic$5 adj program$5 adj guide$5))) and ((725/95).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:42
15	0	((bit adj rate$5) same (frequenc$6 bandwidth) same (reserv$6 allocat$6) and (epg (electronic$5 adj program$5 adj guide$5))) and ((709/232 709/233).ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:42
13	2	((bit adj rate$5) same (frequenc$6 bandwidth) same (reserv$6 allocat$6) and (epg (electronic$5 adj program$5 adj guide$5))) and ((725/39).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:42
-	1	("6549929").PN.	USPAT	2004/09/16 11:22
-	5	(("6208799") or ("6177931") or ("6167379") or ("5956455") or ("5844181")).PN.	USPAT	2004/09/16 12:20
-	0	((("6208799") or ("6177931") or ("6167379") or ("5956455") or ("5844181")).PN.) and (tivo)	USPAT	2004/09/16 13:06
-	2	(("4949251") or ("5113380")).PN.	USPAT	2004/09/16 11:27
-	0	((("4949251") or ("5113380")).PN.) and (tivo)	USPAT	2004/09/16 11:27
-	1	((("6208799") or ("6177931") or ("6167379") or ("5956455") or ("5844181")).PN.) and (frequenc$9)	USPAT	2004/09/16 13:10
-	33	(frequenc$6 adj band$5) same (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 13:19
-	291	(frequenc$6 adj band$5) with (viewer$5 customer$5 user$5) with (allocat$6 reserv$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 13:18
-	221	(frequenc$6 adj band$5) near9 (viewer$5 customer$5 user$5) near9 (allocat$6 reserv$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 13:23

-	3	((frequenc$6 adj band$5) near9 (viewer$5 customer$5 user$5) near9 (allocat$6 reserv$6)) and (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:31
-	259	((frequenc$6 adj band$5) (transmission adj channel$5)) near9 (viewer$5 customer$5 user$5) near9 (allocat$6 reserv$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 13:23
-	3	(((frequenc$6 adj band$5) (transmission adj channel$5)) near9 (viewer$5 customer$5 user$5) near9 (allocat$6 reserv$6)) and (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:27
-	266	(frequenc$5 adj band$5) near9 (bit adj rate$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:27
-	13	(frequenc$5 adj band$5) near9 (bit adj rate$5) near9 (allocat$6 reserv$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:38
-	30	(frequenc$5 adj (channel$5 band$5)) with (bit$5 near rate$5) with (allocat$6 reserv$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:32
-	0	((frequenc$5 adj (channel$5 band$5)) with (bit$5 near rate$5) with (allocat$6 reserv$6)) and (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:32
-	536	(frequenc$5 adj (channel$5 band$5)) with (bit$5 near rate$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:42
-	2	((frequenc$5 adj (channel$5 band$5)) with (bit$5 near rate$5)) and (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:43
-	266	(frequenc$5 adj band$5) near9 (bit adj rate$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:39
-	0	(frequenc$5 adj band$5) near3 (bit adj rate$5) near3 (mbps)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:39
-	1	(frequenc$5 adj band$5) near9 (bit adj rate$5) near9 (mbps)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:39
-	136	(frequenc$5 adj band$5) near4 (bit adj rate$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:42
-	1036	(speed$5) near3 (bit adj rate$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:42

-	16325	((bit adj rate$5) speed$6) with (reserv$6 allocat$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:44
-	71	(((bit adj rate$5) speed$6) with (reserv$6 allocat$6)) and (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:37
-	712	((bit adj rate$5) speed$6) with (reserv$6 allocat$6) with (customer$6 user$5 subscriber$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/16 14:44
-	4	(((bit adj rate$5) speed$6) with (reserv$6 allocat$6) with (customer$6 user$5 subscriber$6)) and (epg (electronic$5 adj program$5 adj guide$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:43
-	0	((av (audio adj video)) adj2 network$3) near8 resource$5 near8 reserv$9 near8 (manag$6 table$5)	USPAT	2004/09/17 13:04
-	0	(frequenc$5 adj band$5) with (reserv$6 allocat$6) with (program$3 adj guide$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:17
-	0	(frequenc$5 adj band$5) same (reserv$6 allocat$6) same (program$3 adj guide$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:17
-	3067	(frequenc$5 adj band$5) near6 (reserv$6 allocat$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:18
-	193	((frequenc$5 adj band$5) near6 (reserv$6 allocat$6)) and (725/$).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:18
-	11	(frequenc$5 adj band$5) near6 (reserv$6 allocat$6) near9 ((bit$5 adj rat$6) mpbs)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:20
-	2	(((bit adj rate$5) speed$6) with (reserv$6 allocat$6) with (customer$6 user$5 subscriber$6)) and (epg (electronic$5 adj program$5 adj guide$5)) and bandwidth	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:57
-	963	(reserv$6 near3 (schedule$5 time$5) near3 program$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:02
-	74	reserv$6 near3 time$5 near3 program$5 near3 user$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 14:02

Terms used **allocate bandwith frequency bit rate** Found **16** of **142,346**

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Relevance scale

1 System-level power optimization: techniques and tools
Luca Benini, Giovanni de Micheli
April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 5 Issue 2
Full text available: pdf(385.22 KB) Additional Information: full citation, abstract, references, citings, index terms

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic sytems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survery ...

2 Adaptive realtime bandwidth allocation for wireless data delivery
Chi-Wai Lin, Haibo Hu, Dik-Lun Lee
March 2004 **Wireless Networks**, Volume 10 Issue 2
Full text available: pdf(372.26 KB) Additional Information: full citation, abstract, references, index terms

The combination of broadcast and on-demand data delivery services is an economic way to build a highly scalable wireless information system with limited bandwidth. The use of data broadcasting should be adaptive so that the system response time can always be minimized. A traditional approach requires the development of a system response time equation in order to find the optimal solution. However, obtaining such an equation is not always possible. We observe that by maintaining a certain level o ...

Keywords: bandwidth allocation, mobile computing, on-demand broadcasting

3 Provision of multimedia services over Europe by means of geostationary satellites with multispot coverage using small terminals
Michele Luglio, Mauro Marinelli, Aldo Paraboni
February 1998 **Wireless Networks**, Volume 4 Issue 2
Full text available: pdf(2.09 MB) Additional Information: full citation, abstract, references, index terms, review

The possibility to provide multimedia services over a wide area using small mobile terminals represents a challenging task. This task is even more challenging especially if high frequency bands are used and high service availability is required, considering that the

atmospheric conditions are very severe and geographically variable within a wide area. The use of high frequencies is necessary to provide wideband services at high data rate for a large number of users and the link availability ...

4 Transmission of video telephony images over wireless channels

Hang Liu, Magda El Zarki

August 1996 **Wireless Networks**, Volume 2 Issue 3

Full text available: pdf(367.28 KB) Additional Information: full citation, abstract, references, index terms

In this paper, the effects of digital transmission errors on H.263 codecs are analyzed and the transmission of H.263 coded video over a TDMAradio link is investigated. The impact of channel coding and interleaving on video transmission quality is simulated for different channel conditions. Fading on radio channels causes significant transmission errors and H.263 coded bit streams are very vulnerable to errors. Powerful Forward Error Correction (FEC) codes are therefore necessary to protect ...

5 Session 9: Parallel scheduling problems in next generation wireless networks

L. Becchetti, S. Diggavi, S. Leonardi, A. Marchetti-Spaccamela, S. Muthukrishnan, T. Nandagopal, A. Vitaletti

August 2002 **Proceedings of the fourteenth annual ACM symposium on Parallel algorithms and architectures**

Full text available: pdf(330.22 KB) Additional Information: full citation, abstract, references, index terms

Next generation 3G/4G wireless data networks allow multiple codes (or channels) to be allocated to a single user, where each code can support multiple data rates. Providing fine-grained QoS to users in such networks poses the two dimensional challenge of assigning both power (rate) and codes for every user. This gives rise to a new class of parallel scheduling problems. We abstract general downlink scheduling problems suitable for proposed next generation wireless data systems. This includes a c ...

6 Efficient use of memory bandwidth to improve network processor throughput

Jahangir Hasan, Satish Chandra, T. N. Vijaykumar

May 2003 **ACM SIGARCH Computer Architecture News , Proceedings of the 30th annual international symposium on Computer architecture**, Volume 31 Issue 2

Full text available: pdf(184.83 KB) Additional Information: full citation, abstract, references

We consider the efficiency of packet buffers used in packet switches built using network processors (NPs). Packet buffers are typically implemented using DRAM, which provides plentiful buffering at a reasonable cost. The problem we address is that a typical NP workload may be unable to utilize the peak DRAM bandwidth. Since the bandwidth of the packet buffer is often the bottleneck in the performance of a shared-memory packet switch, inefficient use of available DRAM bandwidth further reduces th ...

7 Single-ISA Heterogeneous Multi-Core Architectures for Multithreaded Workload Performance

June 2004 **Proceedings of the 31st annual international symposium on Computer architecture - Volume 00**

Full text available: pdf(223.32 KB) Publisher Site Additional Information: full citation, abstract

A single-ISA heterogeneous multi-core architecture is achip multiprocessor composed of cores of varying size, performance,and complexity. This paper demonstrates that thisarchitecture can provide significantly higher performance inthe same area than a conventional chip multiprocessor. It doesso by matching the various jobs of a diverse workload to thevarious cores. This type of architecture covers a spectrum ofworkloads particularly well, providing high single-thread performancewhen thread paral ...

8 Software trace cache
Alex Ramírez, Josep-L. Larriba-Pey, Carlos Navarro, Josep Torrellas, Mateo Valero
May 1999 **Proceedings of the 13th international conference on Supercomputing**
Full text available: pdf(1.12 MB) Additional Information: full citation, references, citings, index terms

9 Multiprocessors with a serial multiport memory and a pseudo crossbar of serial links used s a processor-memeory switch
Daniel Litaize, Omar Hammami, Mustapha Lalam, Adelaziz Mzoughi, Pascl Sinrat
December 1989 **ACM SIGARCH Computer Architecture News**, Volume 17 Issue 6
Full text available: pdf(1.07 MB) Additional Information: full citation, abstract, citings, index terms

This paper presents an inventive information exchange pro-cess between the main memory and cache equipped processors. It makes use of serial multiport memories and high throughput serial transmission supports. It is then possible to consider the realization of a multiprocessor with a common memory shared by several hundreds processors set with a performance level close to that of a crossbar network one's without having its disadvantages. This exchange process generates a family of possible archi ...

10 Evaluating the performance of a unified switching node using a simulated network
Kenneth J. Bodzioch, Bernard E. Patrusky
December 1976 **Proceedings of the 76 Bicentennial conference on Winter simulation**
Full text available: pdf(750.45 KB) Additional Information: full citation, abstract, references, index terms

This paper describes a program which utilizes a discrete event simulation to drive a real switching node. Empirical measurements of various nodal performance characteristics are gathered and recorded during the exercise of this program to aid in the evaluation and design of candidate future nodal architectures. Applications software and specialized hardware for a unified node which switches both digitized voice and data (packet) traffic were developed and tested in a flexible tes ...

11 Access pattern-based memory and connectivity architecture exploration
Peter Grun, Nikil Dutt, Alex Nicolau
February 2003 **ACM Transactions on Embedded Computing Systems (TECS)**, Volume 2 Issue 1
Full text available: pdf(857.06 KB) Additional Information: full citation, abstract, references, index terms

Memory accesses represent a major bottleneck in embedded systems power and performance. Traditionally, designers tried to alleviate this problem by relying on a simple cache hierarchy, or a limited use of special purpose memory modules such as stream buffers. Although real-life applications contain a large number of memory references to a diverse set of data structures, a significant percentage of all memory accesses in the application are generated from a few memory instructions that exhibit pr ...

Keywords: Memory, access patterns, architecture exploration

12 Traffic characterization: Characteristics of fragmented IP traffic on internet links
Colleen Shannon, David Moore, k claffy
November 2001 **Proceedings of the First ACM SIGCOMM Workshop on Internet Measurement**
Full text available: pdf(2.36 MB) Additional Information: full citation, abstract, references, citings, index terms

Fragmented IP traffic is a unique component of the overall mix of traffic on the Internet. Many assertions about the nature and extent of fragmented traffic are anecdotal rather than empirical. In this paper we examine the causes and attributes of measured fragment traffic and contrast those results with commonly cited beliefs. In particular, the effects of NFS, streaming media, networked video games, and tunneled traffic are quantified, and we estimate the prevalence of packet fragmentation due ...

Keywords: CoralReef, TCP/IP, fragment, fragmentation

13 Programming the FlexRAM parallel intelligent memory system

Basilio B. Fraguela, Jose Renau, Paul Feautrier, David Padua, Josep Torrellas

June 2003 **ACM SIGPLAN Notices , Proceedings of the ninth ACM SIGPLAN symposium on Principles and practice of parallel programming**, Volume 38 Issue 10

Full text available: pdf(256.04 KB) Additional Information: full citation, abstract, references, index terms

In an intelligent memory architecture, the main memory of a computer is enhanced with many simple processors. The result is a highly-parallel, heterogeneous machine that is able to exploit computation in the main memory. While several instantiations of this architecture have been proposed, the question of how to effectively program them with little effort has remained a major challenge.In this paper, we show how to effectively hand-program an intelligent memory architecture at a high level and w ...

Keywords: compiler directives, intelligent memory architecture, parallel languages, programming heterogeneous computers

14 Online multicast routing with bandwidth guarantees: a new approach using multicast network flow

Murali Kodialam, T. V. Lakshman, Sudipta Sengupta

August 2003 **IEEE/ACM Transactions on Networking (TON)**, Volume 11 Issue 4

Full text available: pdf(414.82 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a new algorithm for online routing of bandwidth-guaranteed multicasts where routing requests arrive one by one without there being any *a priori* knowledge of future requests. A multicast routing request consists of a source s, a set of receivers R, and a bandwidth requirement b. This multicast routing problem arises in many contexts. Two applications of interest are routing of point-to-multipoint label-switched paths in multiprotocol label switched ...

Keywords: MPLS, multicast, quality-of-service (QoS), routing

15 Online multicast routing with bandwidth guarantees: a new approach using multicast network flow

Murali S. Kodialam, T. V. Lakshman, Sudipta Sengupta

June 2000 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 2000 ACM SIGMETRICS international conference on Measurement and modeling of computer systems**, Volume 28 Issue 1

Full text available: pdf(883.76 KB) Additional Information: full citation, abstract, references, index terms

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Keywords: Steiner tree, multicast routing, network flow, traffic engineering

16 Contention is no obstacle to shared-memory multiprocessing

Randall Rettberg, Robert Thomas

December 1986 **Communications of the ACM**, Volume 29 Issue 12

Full text available: pdf(960.61 KB) Additional Information: full citation, abstract, references, citings, index terms, review

Contention in large-scale shared-memory systems is less of a concern than generally believed. Through careful engineering of a parallel system, the effects of contention can be controlled.

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Published before June 1999
Terms used **allocate bandwith frequency bit rate**

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1 Provision of multimedia services over Europe by means of geostationary satellites with multispot coverage using small terminals
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February 1998 **Wireless Networks**, Volume 4 Issue 2

Full text available: pdf(2.09 MB) Additional Information: full citation, abstract, references, index terms, review

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August 1996 **Wireless Networks**, Volume 2 Issue 3

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May 1999 **Proceedings of the 13th international conference on Supercomputing**

Full text available: pdf(1.12 MB) Additional Information: full citation, references, citings, index terms

4 Multiprocessors with a serial multiport memory and a pseudo crossbar of serial links used s a processor-memeory switch
Daniel Litaize, Omar Hammami, Mustapha Lalam, Adelaziz Mzoughi, Pascl Sinrat

December 1989 **ACM SIGARCH Computer Architecture News**, Volume 17 Issue 6

Full text available: pdf(1.07 MB) Additional Information: full citation, abstract, citings, index terms

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Full text available: pdf(750.45 KB) Additional Information: full citation, abstract, references, index terms

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6 Contention is no obstacle to shared-memory multiprocessing

Randall Rettberg, Robert Thomas

December 1986 **Communications of the ACM**, Volume 29 Issue 12

Full text available: pdf(960.61 KB) Additional Information: full citation, abstract, references, citings, index terms, review

Contention in large-scale shared-memory systems is less of a concern than generally believed. Through careful engineering of a parallel system, the effects of contention can be controlled.

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Published before June 1999
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1 The personal electronic program guide—towards the pre-selection of individual TV programs
Michael Ehrmantraut, Theo Härder, Hartmut Wittig, Ralf Steinmetz
November 1996 **Proceedings of the fifth international conference on Information and knowledge management**
Full text available: pdf(923.87 KB) Additional Information: full citation, references, index terms

2 Video anywhere: a system for searching and managing distributed heterogeneous video assets
Amit Sheth, Clemens Bertram, Kshitij Shah
March 1999 **ACM SIGMOD Record**, Volume 28 Issue 1
Full text available: pdf(545.62 KB) Additional Information: full citation, abstract, index terms

Visual information, especially videos, plays an increasing role in our society for both work and entertainment as more sources become available to the user. Set-top boxes are poised to give home users access to videos that come not only from TV channels and personal recordings, but also from the Internet in the form of downloaded and streaming videos of various types. Current approaches such as Electronic Program Guides and video search engines search for video assets of one type or from on ...

3 A contribution to the design process
Klaus B. Bærentsen, Henning Slavensky
May 1999 **Communications of the ACM**, Volume 42 Issue 5
Full text available: pdf(116.38 KB) html(23.73 KB) Additional Information: full citation, references, citings, index terms

4 From custom applications to domain-specific frameworks
Wim Codenie, Koen De Hondt, Patrick Steyaert, Arlette Vercammen
October 1997 **Communications of the ACM**, Volume 40 Issue 10
Full text available: pdf(1.37 MB) Additional Information: full citation, references, citings, index terms

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www.zongoo.com/article8503.html - 35k - Sep 16, 2004 - Cached - Similar pages

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... BRR **Bit Rate** Reduction BSAC **Bit**-Sliced Arithmetic ... Memory EHF Extremely High **Frequency** EIRP Effective ... End OF Message EPG **Electronic Program Guide** ERC European ...
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... an Intel term EPG **Electronic Program Guide** EPROM Erasable ... Blanking Interval VBR Variable **Bit Rate** VBV Video ... Video Display Terminal VF Voice **Frequency** VHF Very ...
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Title - TIX (1):

Resources reservation management apparatus in audio-video networks, judges assignment of bandwidth of reservation program based on reservation demand and communicates when starting time of reservation program is reached

Standard Title Terms - TTX (1):

RESOURCE RESERVE MANAGEMENT APPARATUS AUDIO VIDEO NETWORK JUDGEMENT ASSIGN
BANDWIDTH RESERVE PROGRAM BASED RESERVE DEMAND
COMMUNICATE START TIME RESERVE
PROGRAM REACH